Docket No.: 1592-0169PUS1 (PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Takashi OZAKI et al.

Application No.: 10/549,938 Confirmation No.: 7521

Filed: January 3, 2007 Art Unit: 2895

For: PRODUCING METHOD OF

SEMICONDUCTOR DEVICE AND SUBSTRATE PROCESSING APPARATUS

Examiner: C. Everhart

## STATUS INQUIRY

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir

It is respectfully requested that the representative named below be advised of the status of the Request for Certificate of Correction filed on June 1, 2009. Please advise us of when we might expect to receive the Certificate of Correction from the Patent and Trademark Office.

Dated: MAY 2 5 2010 Respectfully submitted,

Registration No.: 29,680

BIRCH, STEWART, KOLASCH & BIRCH, LLP

8110 Gatehouse Road Suite 100 East

P.O. Box 747

Falls Church, Virginia 22040-0747

(703) 205-8000 Attorney for Applicant